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CONFIRMATION NO. 7189

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APPLICANTS Shinsuke Sadamitsu, Tokyo, JAPAN; Nobumitsu Takase, Tokyo, JAPAN; Hiroyuki Takao, Tokyo, JAPAN; Koji Sueoka, Tokyo, JAPAN; Masataka Horai, Tokyo, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/08333 06/30/2003				
** FOREIGN APPLICATIONS ***** JAPAN 2002-208515 07/17/2002				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged		STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 17
Examiner's Signature _____ Initials _____		INDEPENDENT CLAIMS 4		
ADDRESS 22850				
TITLE High-resistance silicon wafer and process for producing the same				
FILING FEE RECEIVED 1230	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	